

S/N 10/628,594

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Robert C. Sundahl et al.	Examiner:	Donghai Nguyen
Serial No.:	10/628,594	Group Art Unit:	3729
Filed:	July 28, 2003	Docket No:	884.264US2
Title	METHOD FOR MANUFACTURING AN INTERCONNECTED CIRCUIT BOARD ASSEMBLY AND SYSTEM		
Assignee:	Intel Corporation	Customer Number: 21186	

---

**PETITION FOR A ONE-MONTH EXTENSION OF TIME**

MS AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Rejection mailed June 22, 2006, said period of response being extended from September 22, 2006 to October 22, 2006.

**Please charge Deposit Account No. 19-0743 in the amount of \$120.00 to cover the required extension fee. Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.**

Respectfully Submitted

ROBERT C. SUNDALH ET AL.

By their Representatives,  
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Attorneys for Intel Corporation  
P.O. Box 2938  
Minneapolis, Minnesota 55402  
(612) 373-6969

Date: 17 October 2006 By: 

Viet V. Tong  
Reg. No: 45,416

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 17th day of October 2006.

Name: Amy Moriarty Signature: 